

10/537534

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Franz Hofmann et al.
Serial No.: Unknown
(Priority Application No. DE 102 56 486.8)
(International Application No. PCT/DE03/03935)

JCO6 Rec'd PCT/PTO 03 JUN 2005

Nat'l Stage Entry: June 3, 2005
(Priority Date 3 December 2002)
(International Filing Date 27 November 2003)

Docket No.: I432.120.101/P30123

Title: METHOD FOR THE PRODUCTION OF A MEMORY CELL,
MEMORY CELL AND MEMORY CELL ARRANGEMENT

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached form 1449 be considered by the Examiner and made of record. Any required copies of patents, publications or other documents are enclosed for the Examiner's review. Pursuant to the provisions of M.P.E.P. 609, Applicants further request a copy of the 1449 form, marked as being considered and initialed by the Examiner, be returned with the next Official Communication.

Since this Information Disclosure Statement is being submitted within three months of filing national application; or date of entry of national application; or before the mailing date of the first Office Action on the merits, a fee has not been enclosed. However, if such fee is required, the Patent Office is hereby authorized to charge Deposit Account No. 500471 for fees as set forth under 37 C.F.R. 1.17(p).

Information Disclosure Statement

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JC06 Rec'd PCT/PTO 3 JUN 2005

Applicants respectfully request consideration of these references during prosecution of the above-identified matter. The Examiner is invited to contact the Applicants' representative at the below-listed telephone number if there are any questions regarding this Communication or the tendered references.

Respectfully submitted,

Franz Hofmann et al.,

By their attorneys,

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Dated: June 3, 2005
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CERTIFICATE UNDER 37 C.F.R. 1.10:

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Date of Deposit: June 3, 2005

The undersigned hereby certifies that this paper or papers, as described herein, are being deposited in the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to: Mail Stop PCT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22318-1450.

By: Vanessa Carels
Name: Vanessa Carels

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FORM PTO-1449	Docket No.: I432.120.101/P30123	Serial No.: Unknown Priority Appl. No. DE 102 56 486.8 Int'l Appln. No. PCT/DE03/03935
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	Applicant: Franz Hofmann et al.	
	Nat'l Stage Entry: June 3, 2005 Priority Date: 3 December 2002 Int'l Filing Date: 27 November 2003	Group Art: Unknown

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
	5,761,115	06/1998	Kozicki et al.			
	5,896,312	04/1999	Kozicki et al.			
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FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translated Yes No
	1329958	07/2003	EPO			Yes
	1331671	07/2003	EPO			Yes
	03/028124	04/2003	WO			Yes (Abstract Only)
	97/48032	12/1997	WO			Yes

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	Copy of International Search Report having PCT Application No. PCT/DE03/03935 mailed on July 22, 2004.
	Kohlrausch, F., "Practical Physics, vol. 2, 23rd Edition, Teubner Verlag Stuttgart, 1985, pages 31-32, <concise explanation included.>
	Haag, Rainer et al., "Electrical Breakdown of Aliphatic and Aromatic Self-Assembled Monolayers Used as Nanometer-Thick Organic Dielectrics," J. Am. Chem. Soc., 1999, pp. 7895-7906.
	Terabe, K. et al., "Quantum point contact switch realized by solid electrochemical reaction", RIKEN Review, Focused on Nanotechnology in RIKENI, No. 37, July 2001, pp. 7-8.
	Terabe, K. et al., "Formation and disappearance of a nanoscale silver cluster realized by solid electrochemical reaction," Journal of Applied Physics, Vol. 91, No. 12, June 15, 2002, pp. 10110-10114.
	Hofmann, F. et al., "Surrounding Gate Select Transistor for 4F ² Stacked Gbit DRAM," Infineon Technologies Corporate Research, Munich, Germany.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.